

METHOD FOR GENERATING INTERRUPT COMMANDS IN A
MICROPROCESSOR SYSTEM AND RELATIVE PRIORITY INTERRUPT
CONTROLLER

Abstract of the Disclosure

A method for generating interrupt commands for a microprocessor system includes storing interrupts in a pending interrupts register, and storing priority values associated with the stored interrupts in a plurality of priority registers coupled to the pending interrupts register. A plurality of counters coupled in cascade to the plurality of priority registers are loaded with the stored priority values. The loaded priority values are incremented at predetermined intervals, and are compared for identifying the interrupt having a highest priority. The method further includes identifying a respective interrupt service routine to be executed based upon the interrupt having the highest priority.